



# Dynamic Sparse Training with Structured Sparsity

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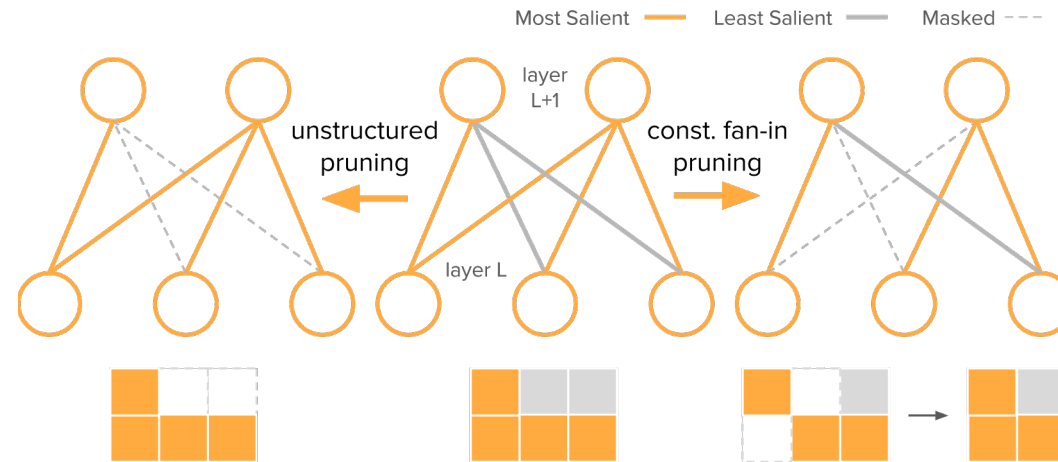


# Motivation

- Unstructured Dynamic Sparse Training (DST) matches the generalization performance of dense models with 85-95% fewer weights
- Accelerating unstructured Sparse Neural Networks (SNNs) is challenging
- Structured SNNs are easy to accelerate, but do not generalize as well as unstructured.
- **Can we use DST to learn a SNN with high generalization performance that is also amenable to acceleration?**

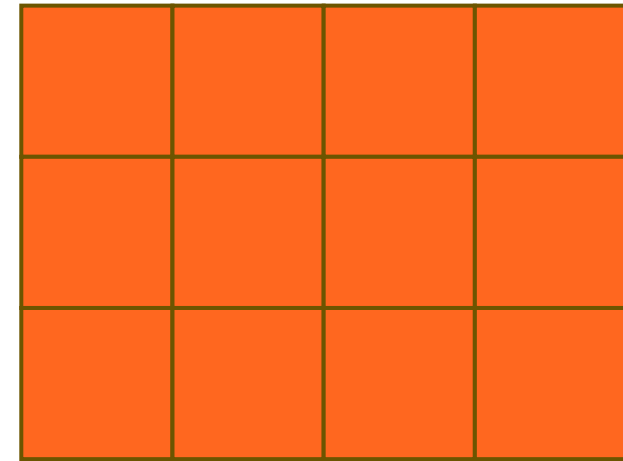
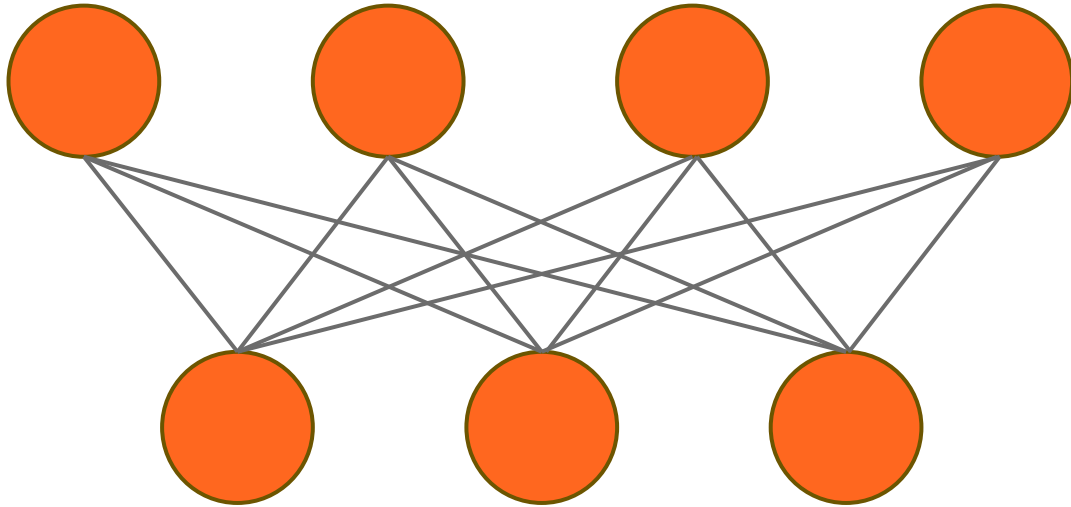


# Method: Structured RigL (SRigL)

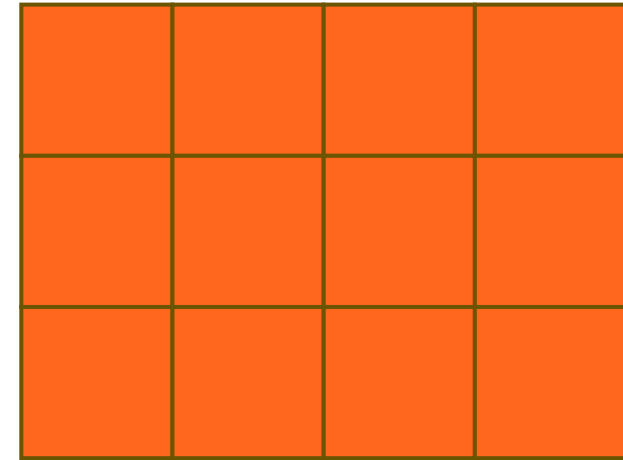
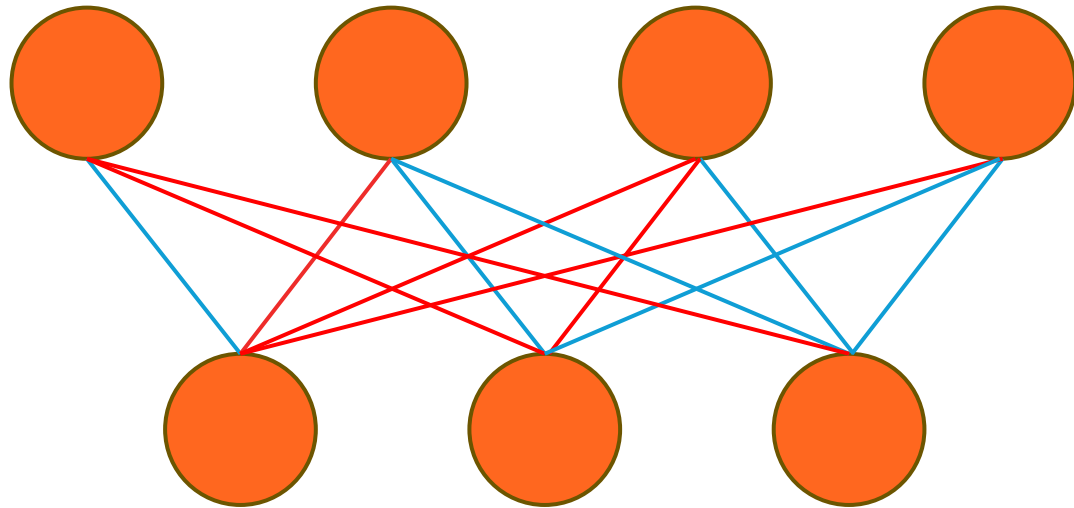


- Sparse-to-sparse DST method which extends RigL to learn a structured SNN
- Learns specific type of **N:M sparsity where M is dense fan-in**
- Constant fan-in constraint applied to each neuron within a given layer to enable efficient and compressible indexing of non-zero weights

# Unstructured vs. Constant Fan-In Sparsity

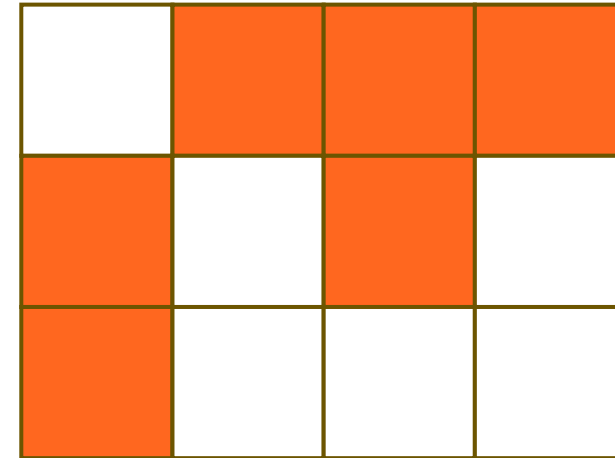
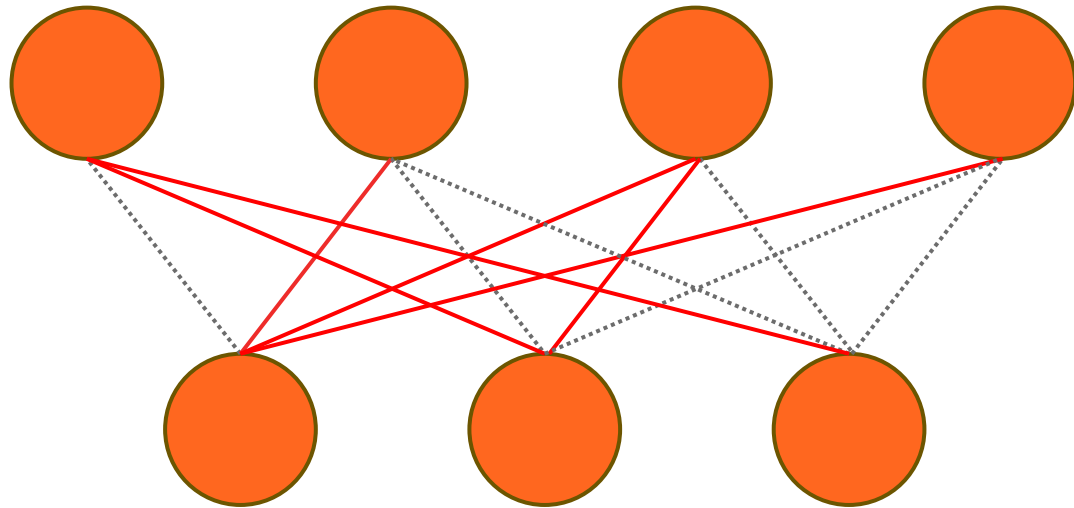


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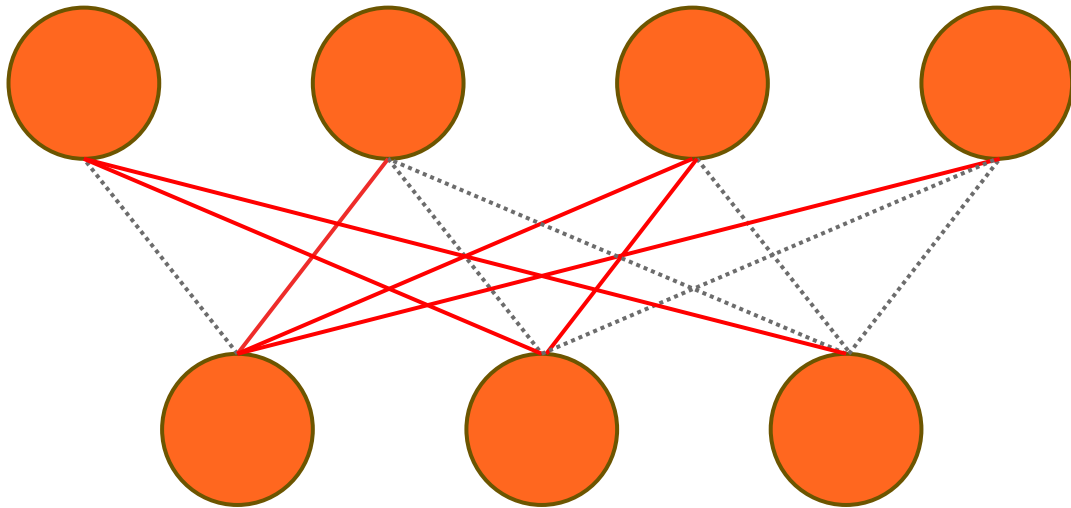
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- • Less Salient
- ..... • Masked

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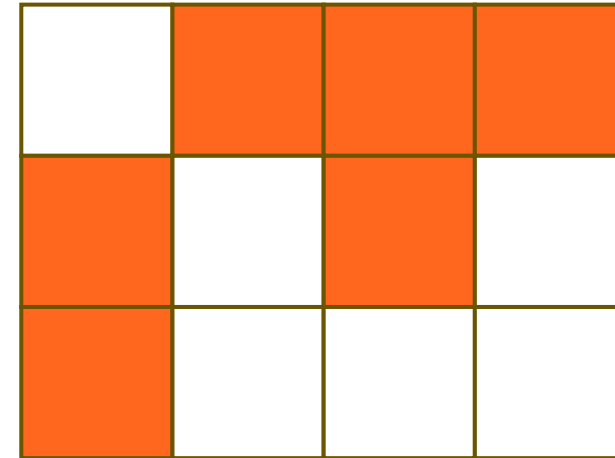


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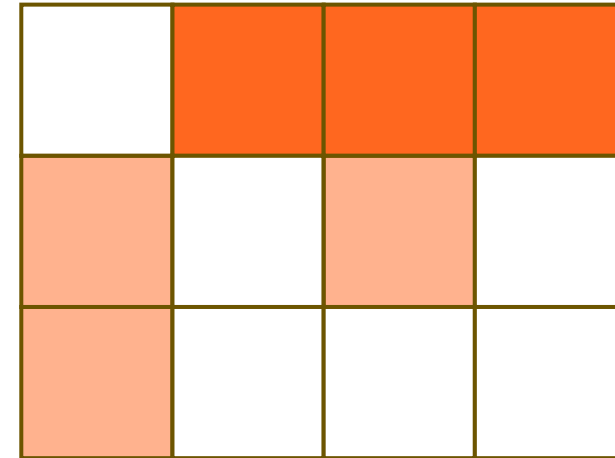
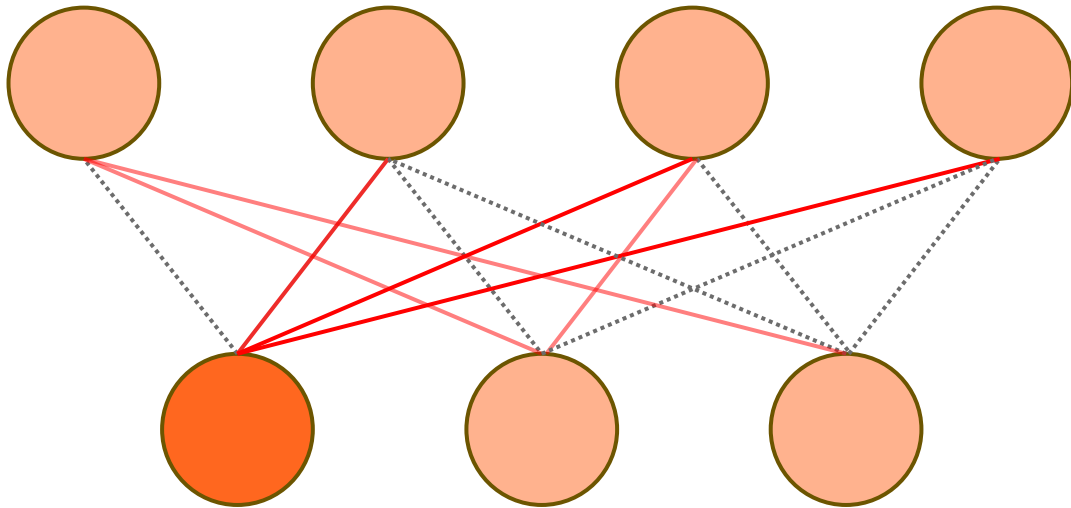


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**Noncontiguous weights make acceleration a challenge!**

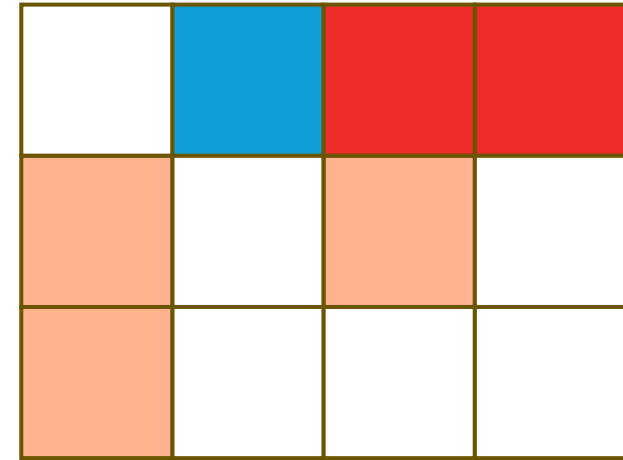
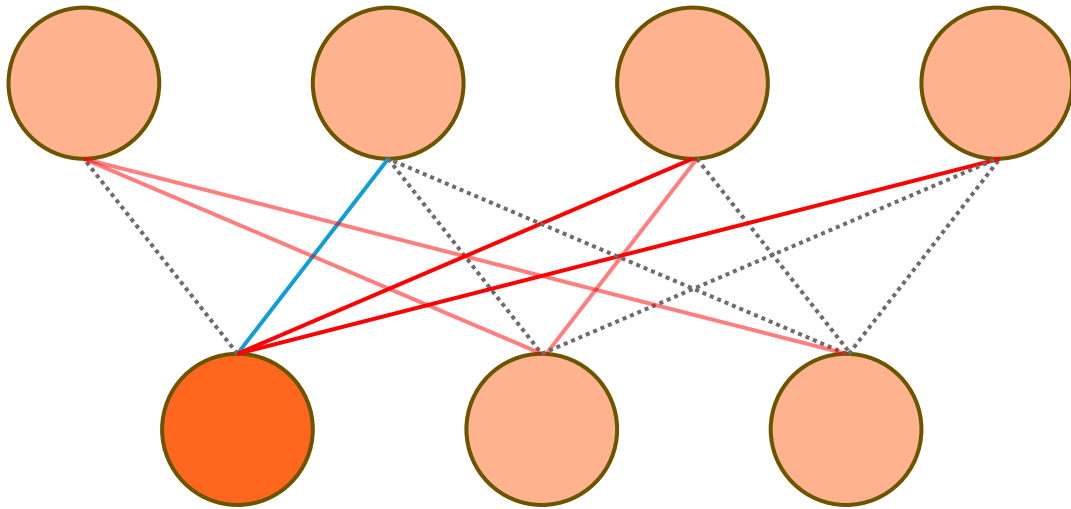
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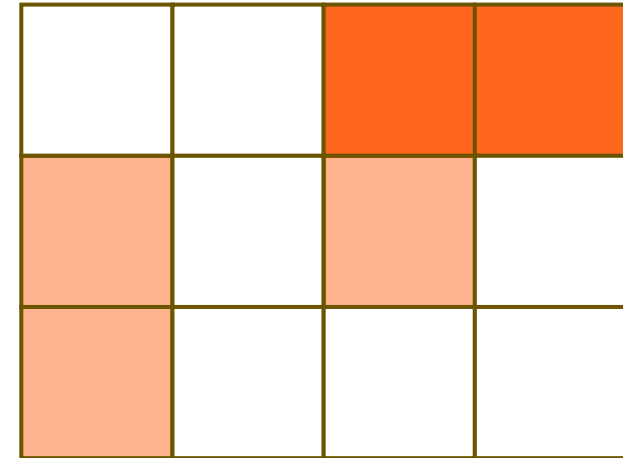
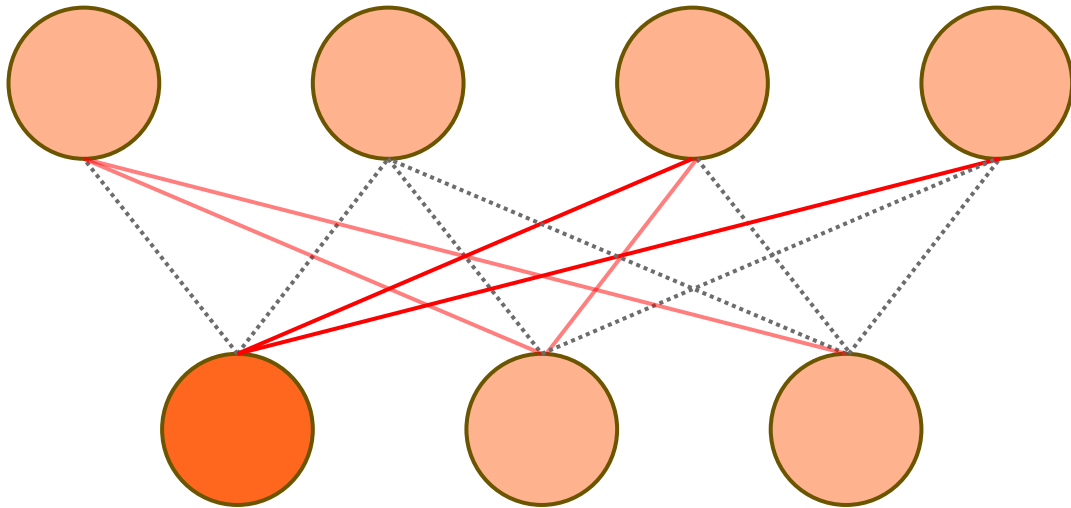


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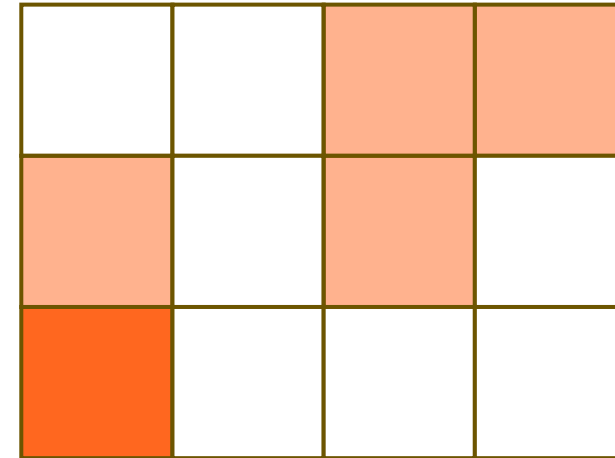
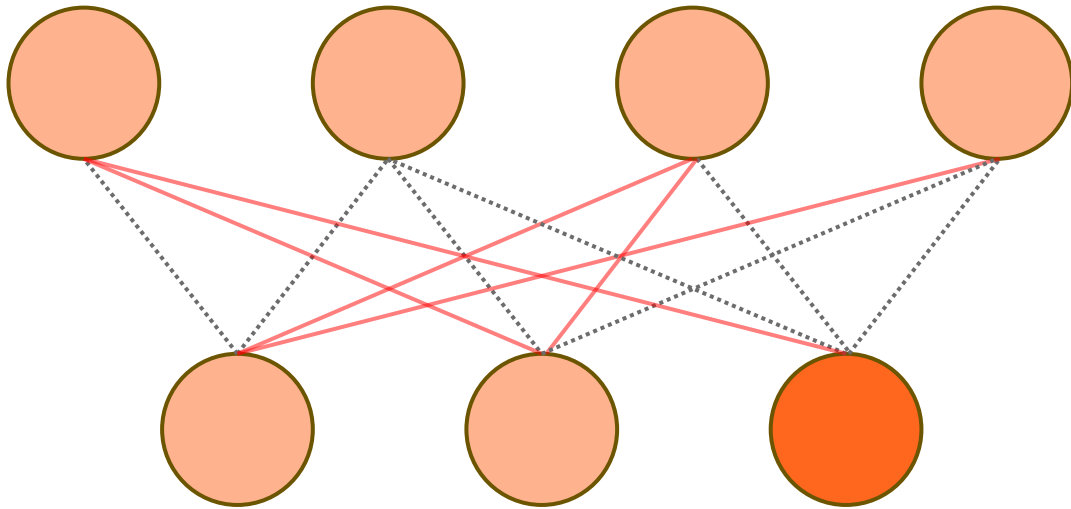
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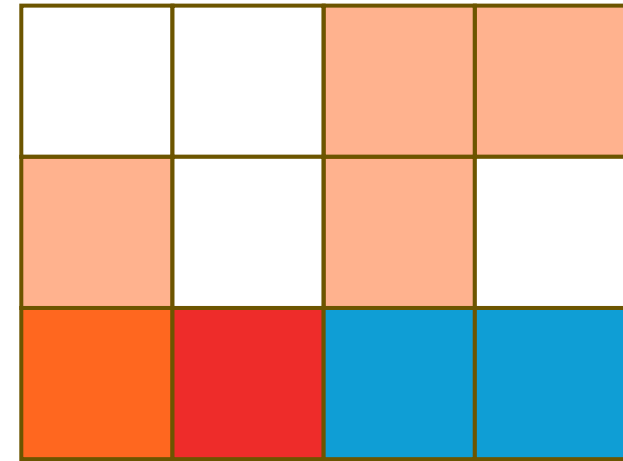
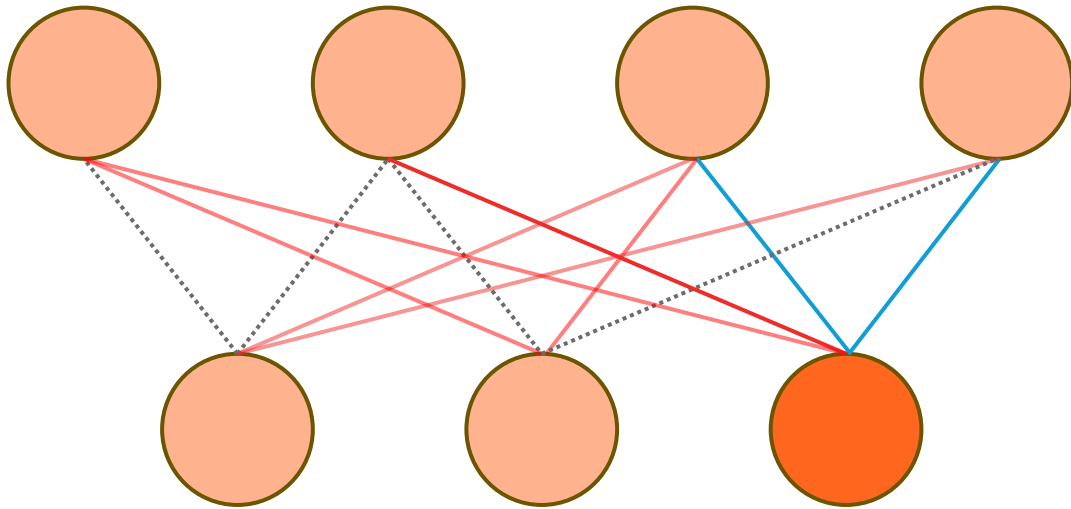
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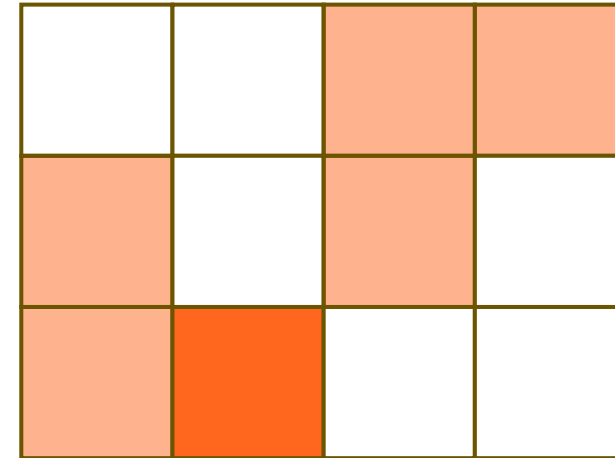
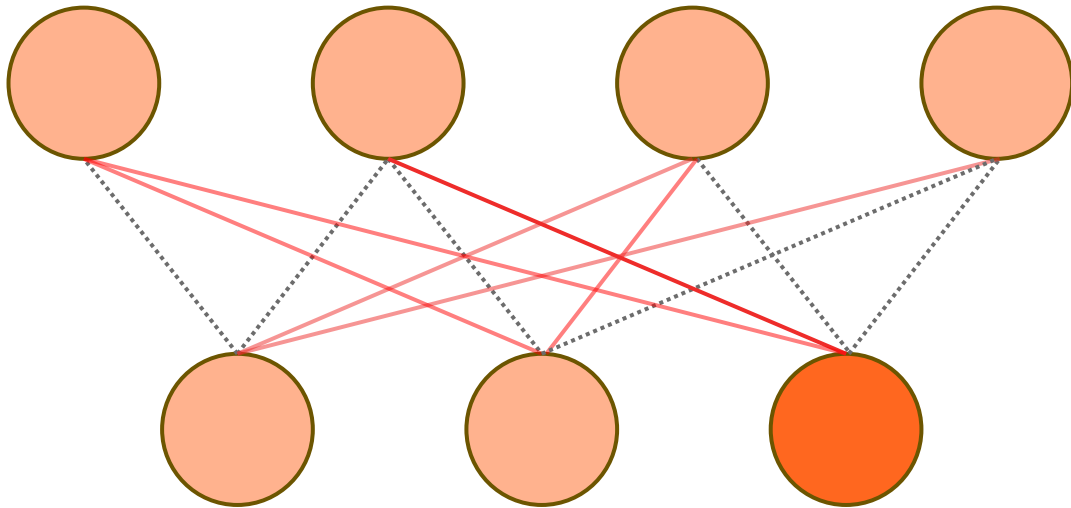
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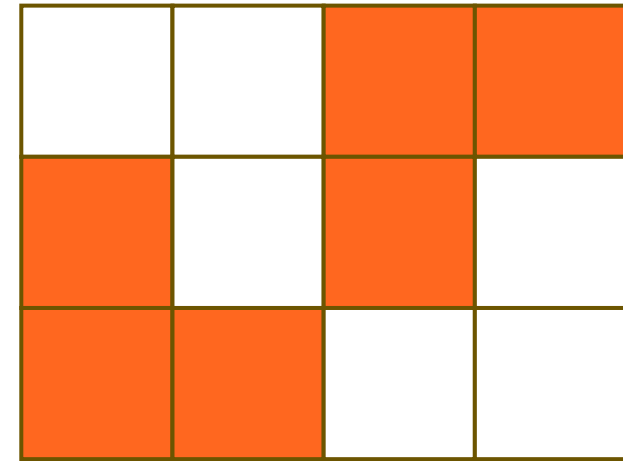
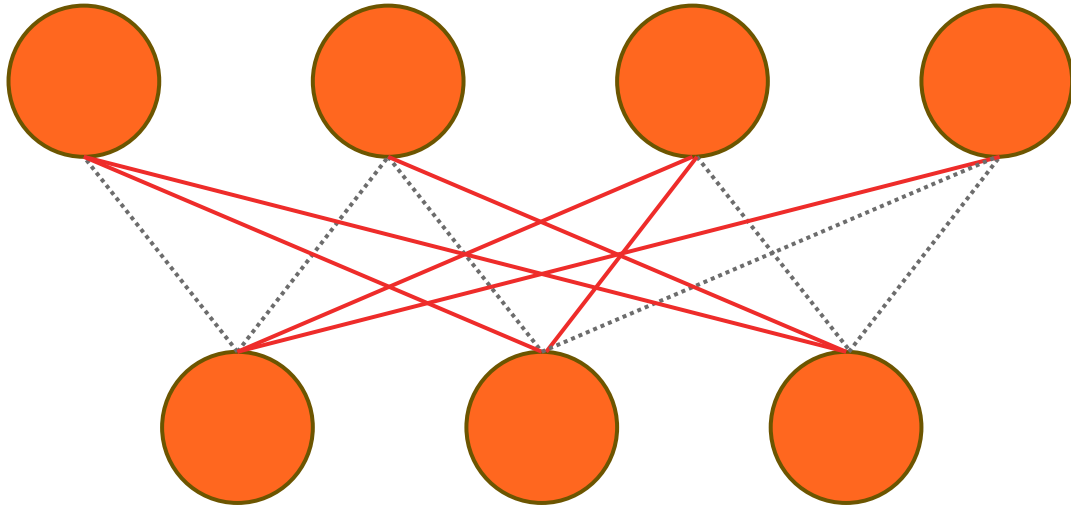
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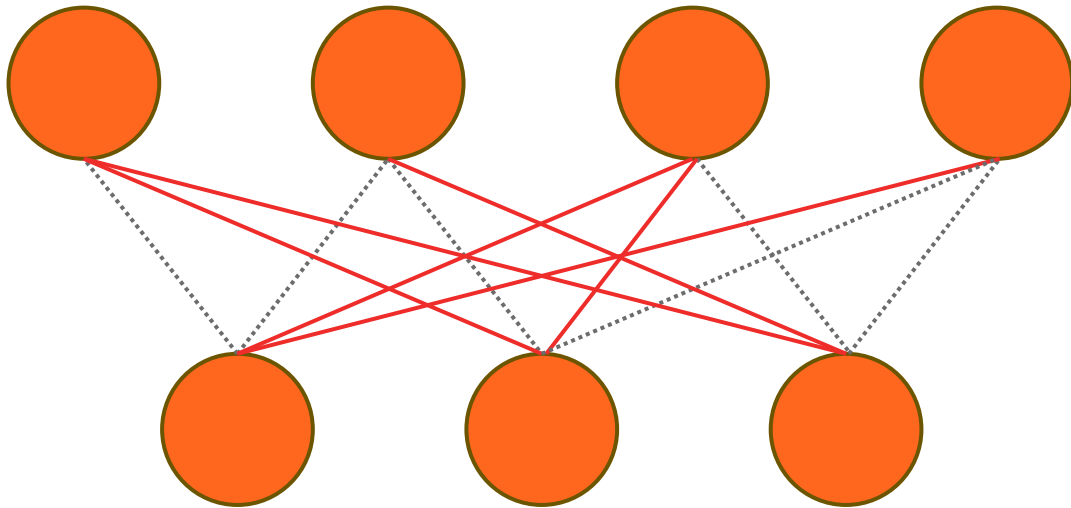
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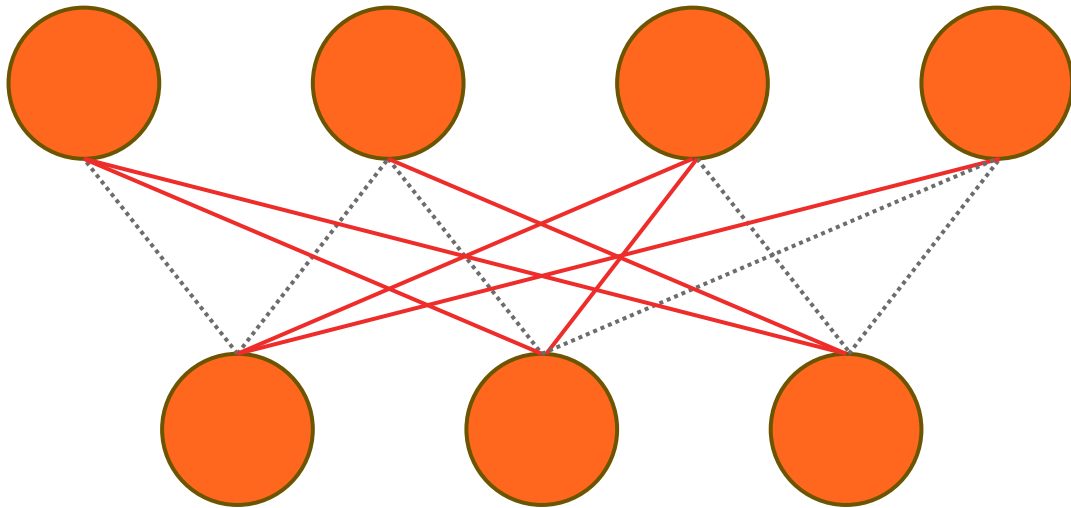
# Unstructured vs. Constant Fan-In Sparsity



	0	1	2	3
0				
1				
2				

- More Salient
- Less Salient
- ..... Masked

# Unstructured vs. Constant Fan-In Sparsity



$W_{0,2}$	$W_{0,3}$
$W_{1,0}$	$W_{1,2}$
$W_{2,0}$	$W_{2,1}$

- More Salient
- Less Salient
- ⋯ Masked



# Constant Fan-In Matrix Multiplication

0	0	$W_{0,2}$	$W_{0,3}$
$W_{1,0}$	0	$W_{1,2}$	0
$W_{2,0}$	$W_{2,1}$	0	0

Sparse Matrix w/  
Constant Fan-in=2

•

$x_0$
$x_1$
$x_2$
$x_3$

# Constant Fan-In Matrix Multiplication

0	0	$W_{0,2}$	$W_{0,3}$
$W_{1,0}$	0	$W_{1,2}$	0
$W_{2,0}$	$W_{2,1}$	0	0

Sparse Matrix w/  
Constant Fan-in=2

 $\cdot$ 

$X_0$
$X_1$
$X_2$
$X_3$

 $=$ 

$0 \cdot X_0 + 0 \cdot X_1 + W_{0,2} \cdot X_2 + W_{0,3} \cdot X_3$
$W_{1,0} \cdot X_0 + 0 \cdot X_1 + W_{1,2} \cdot X_2 + 0 \cdot X_3$
$W_{2,0} \cdot X_0 + W_{2,1} \cdot X_1 + 0 \cdot X_2 + 0 \cdot X_3$

# Constant Fan-In Matrix Multiplication

0	0	$W_{0,2}$	$W_{0,3}$
$W_{1,0}$	0	$W_{1,2}$	0
$W_{2,0}$	$W_{2,1}$	0	0

Sparse Matrix w/  
Constant Fan-in=2

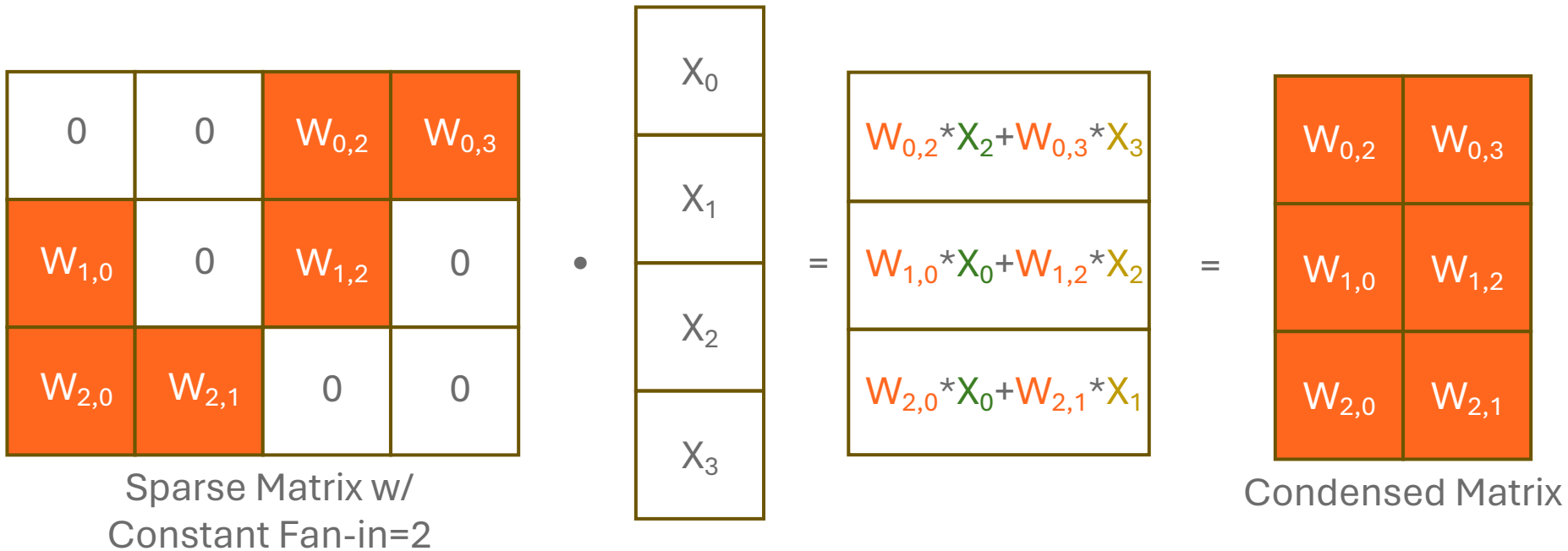
 $\cdot$ 

$X_0$
$X_1$
$X_2$
$X_3$

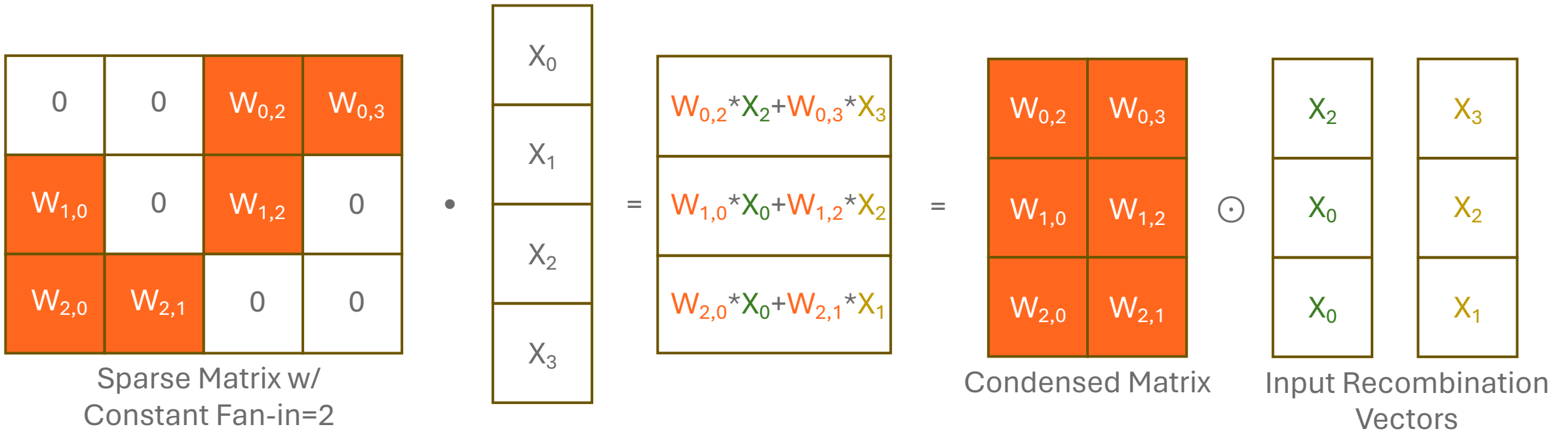
 $=$ 

$W_{0,2} * X_2 + W_{0,3} * X_3$
$W_{1,0} * X_0 + W_{1,2} * X_2$
$W_{2,0} * X_0 + W_{2,1} * X_1$

# Constant Fan-In Matrix Multiplication

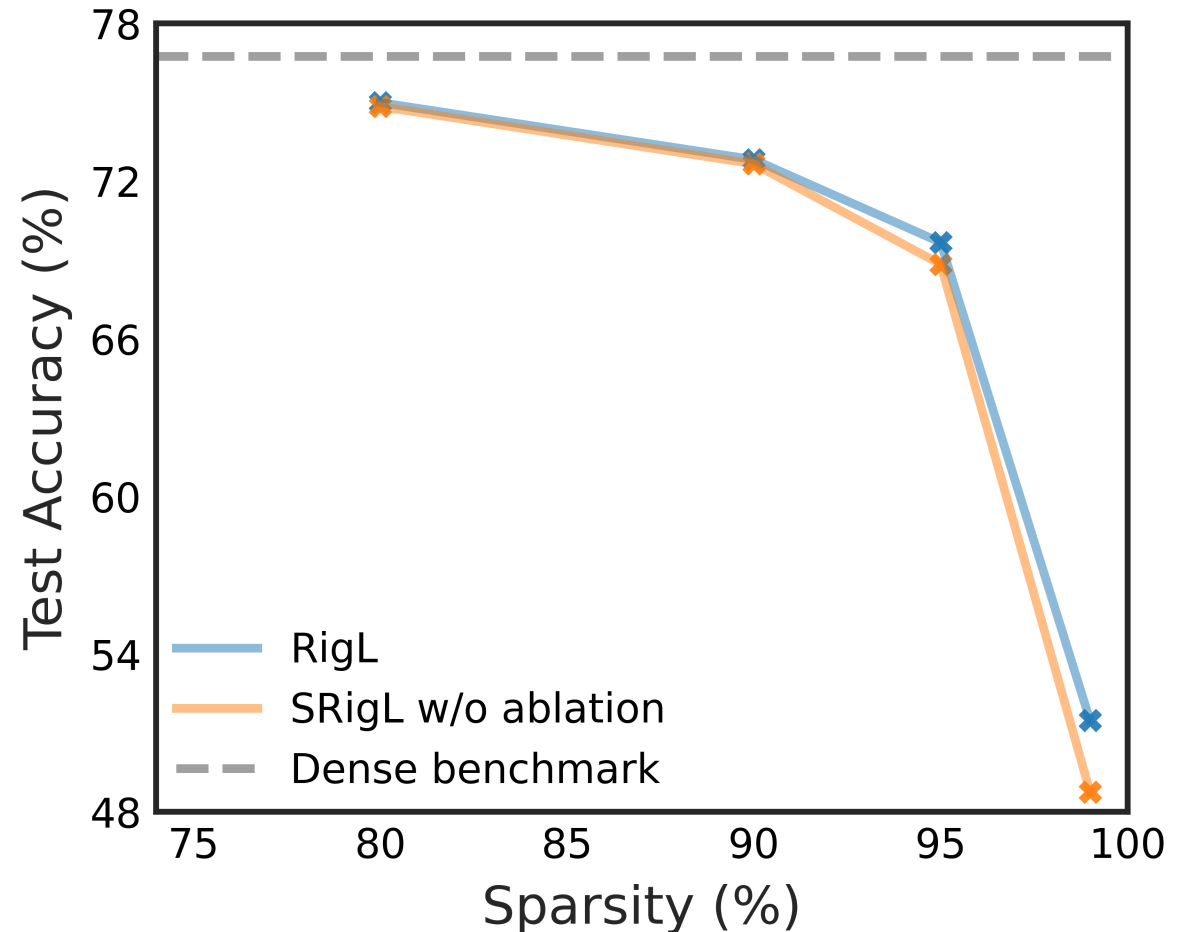


# Constant Fan-In Matrix Multiplication



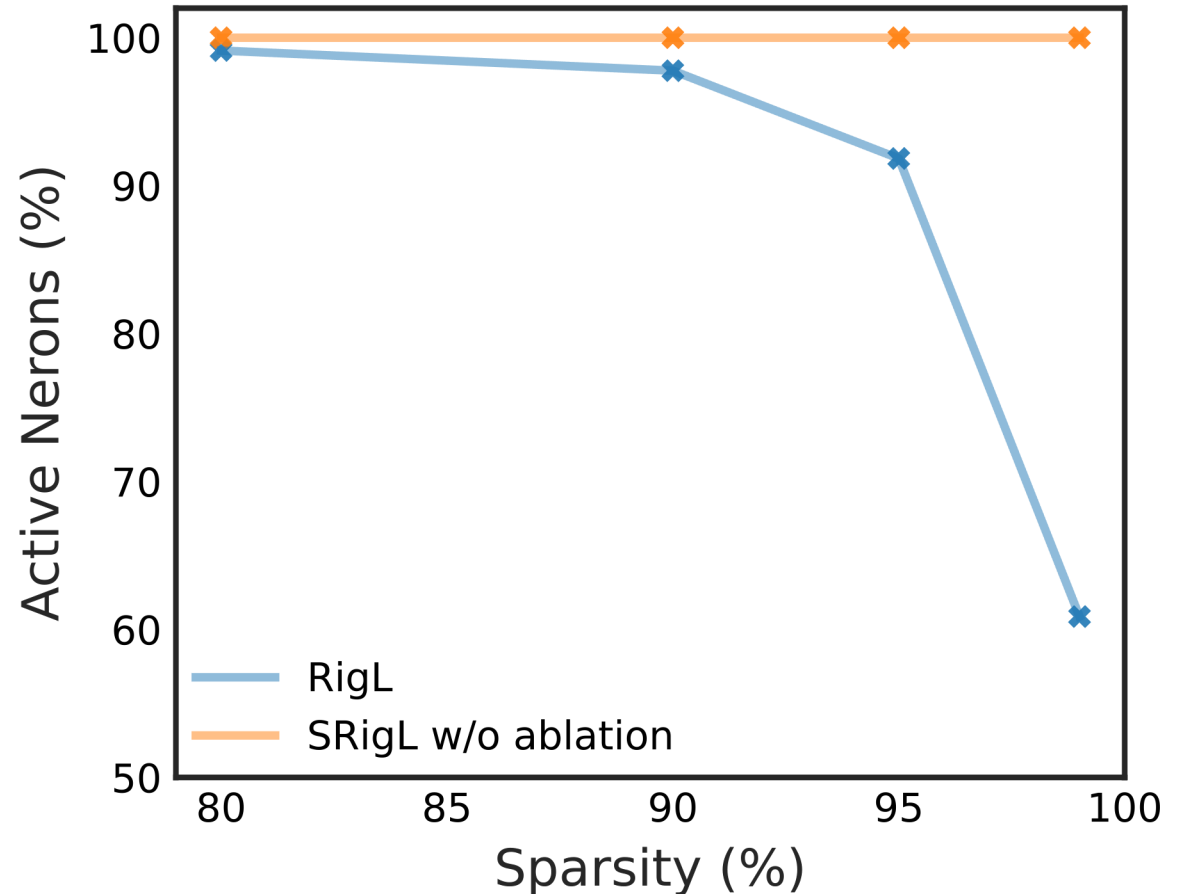
# Initial Results (ImageNet/ResNet50)

- We saw similar generalization with constant fan-in as RigL up to **90% sparsity**
- At high sparsities ( $\geq 90\%$ ) we found constant fan-in did not match RigL results...

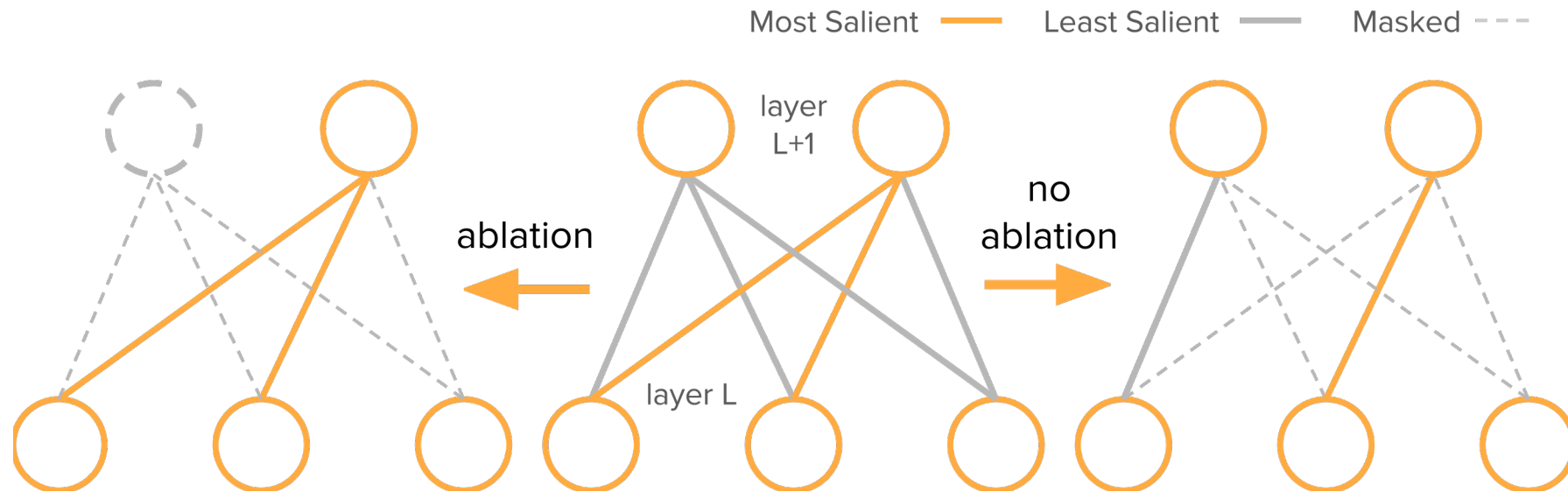


# Neuron Ablation

- At high sparsities ( $\geq 90\%$ ) we found that RigL **ablates many neurons**
- Effectively RigL at high sparsity **learns to reduce the width of layers!**
- However, a naïve constant fan-in constraint **prohibits removal of neurons**, decreasing performance

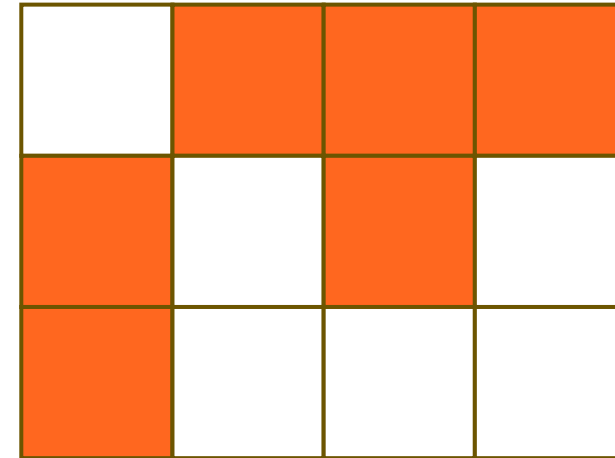
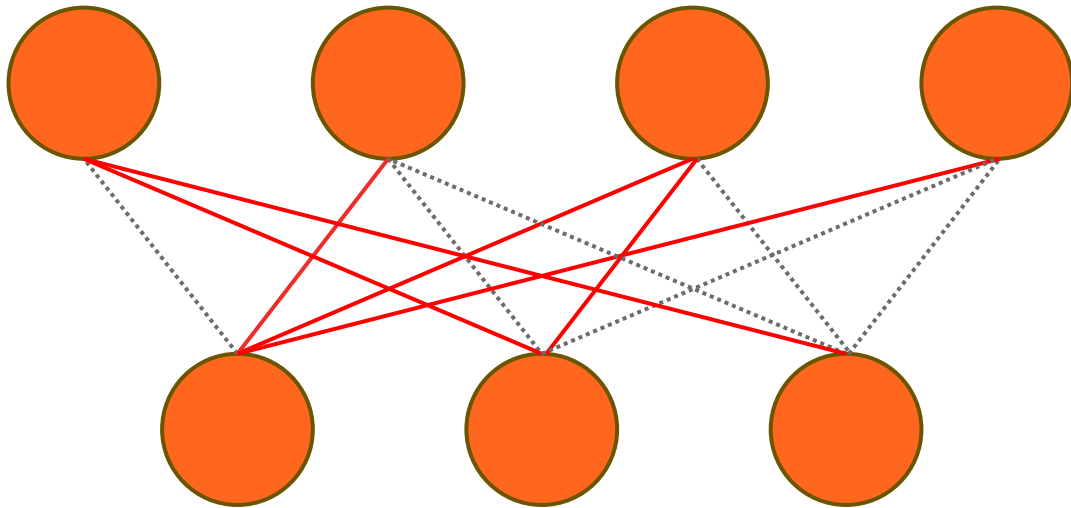


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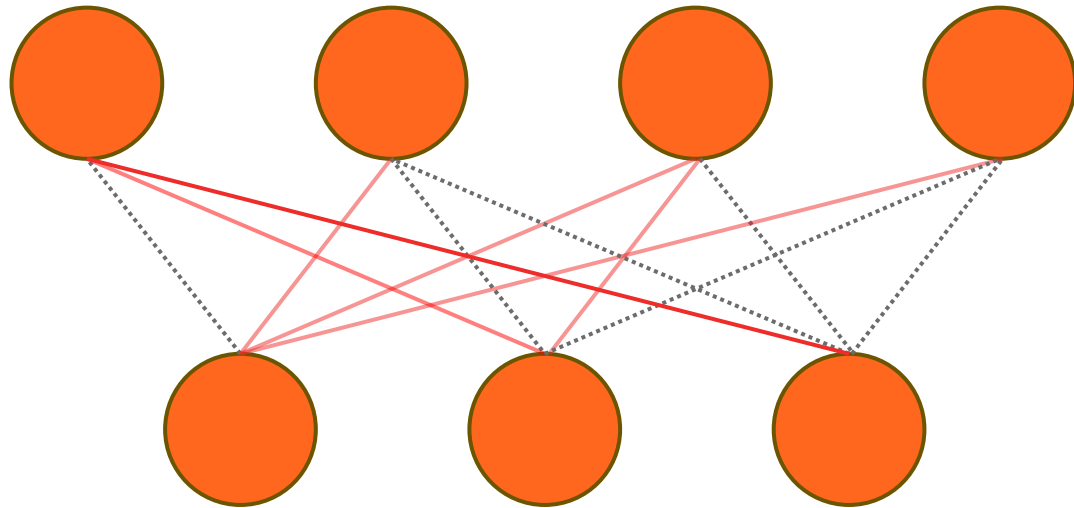




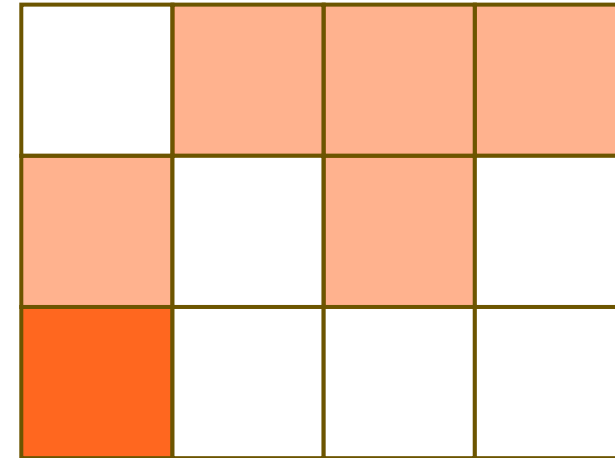
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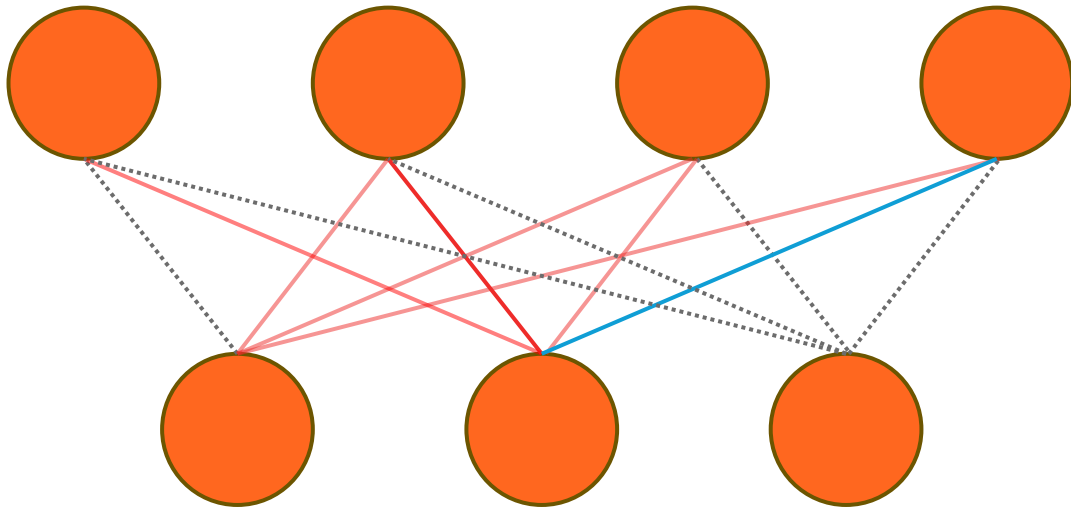
# Neuron Ablation



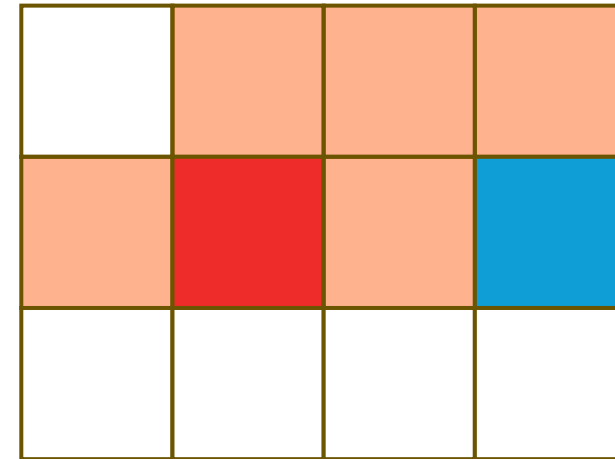
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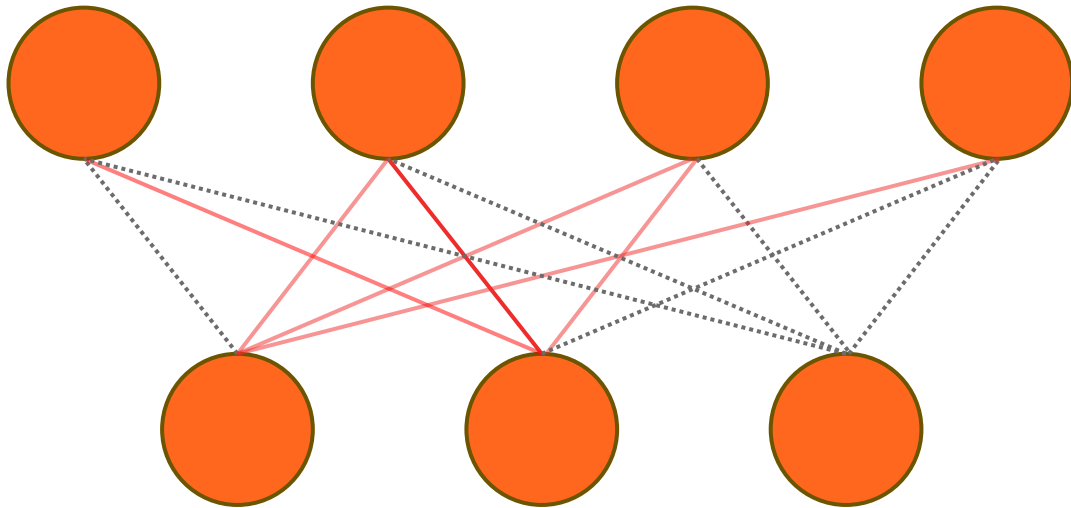
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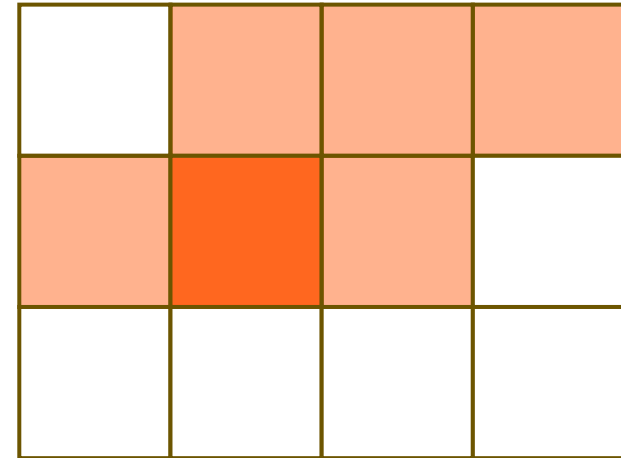
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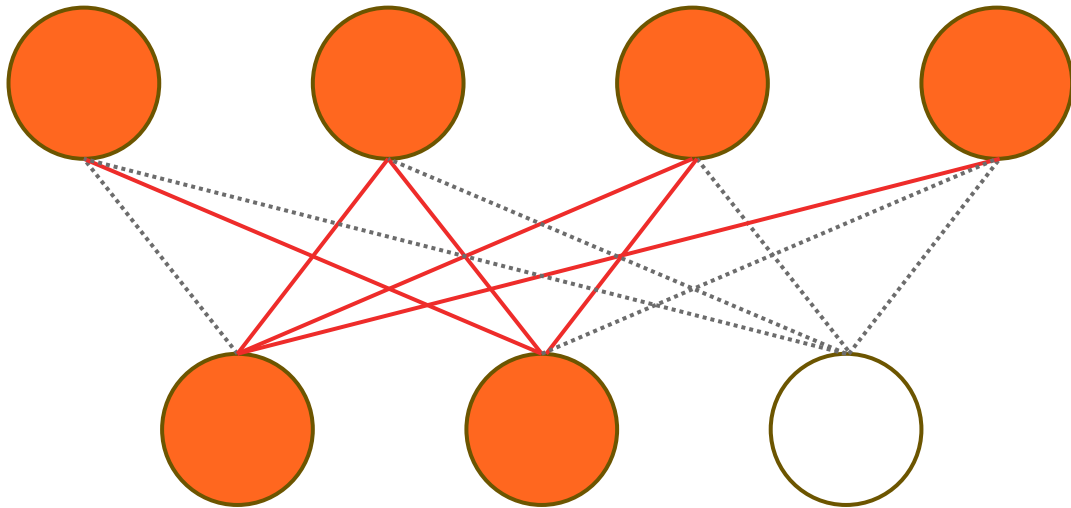
# Neuron Ablation



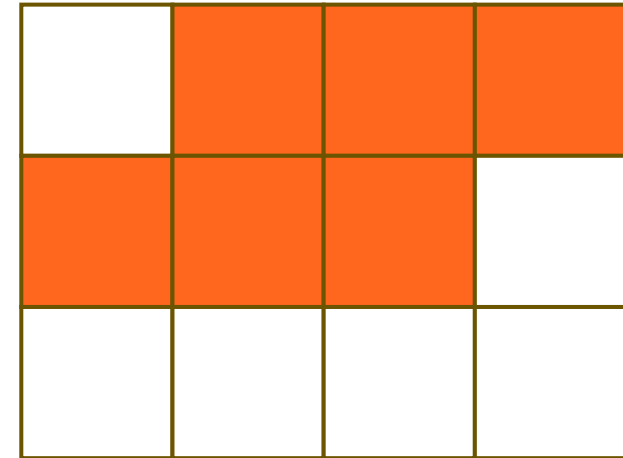
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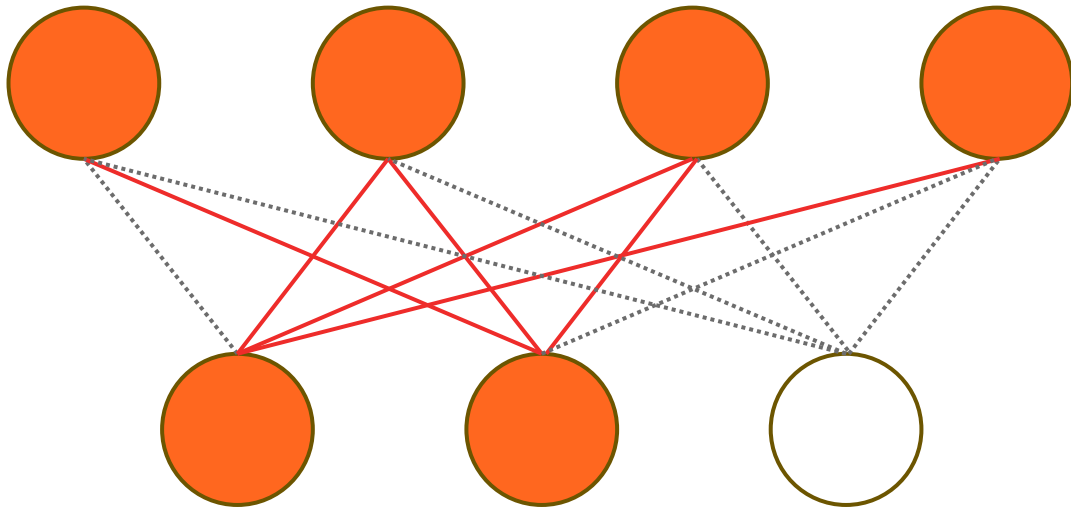
# Neuron Ablation



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# Neuron Ablation

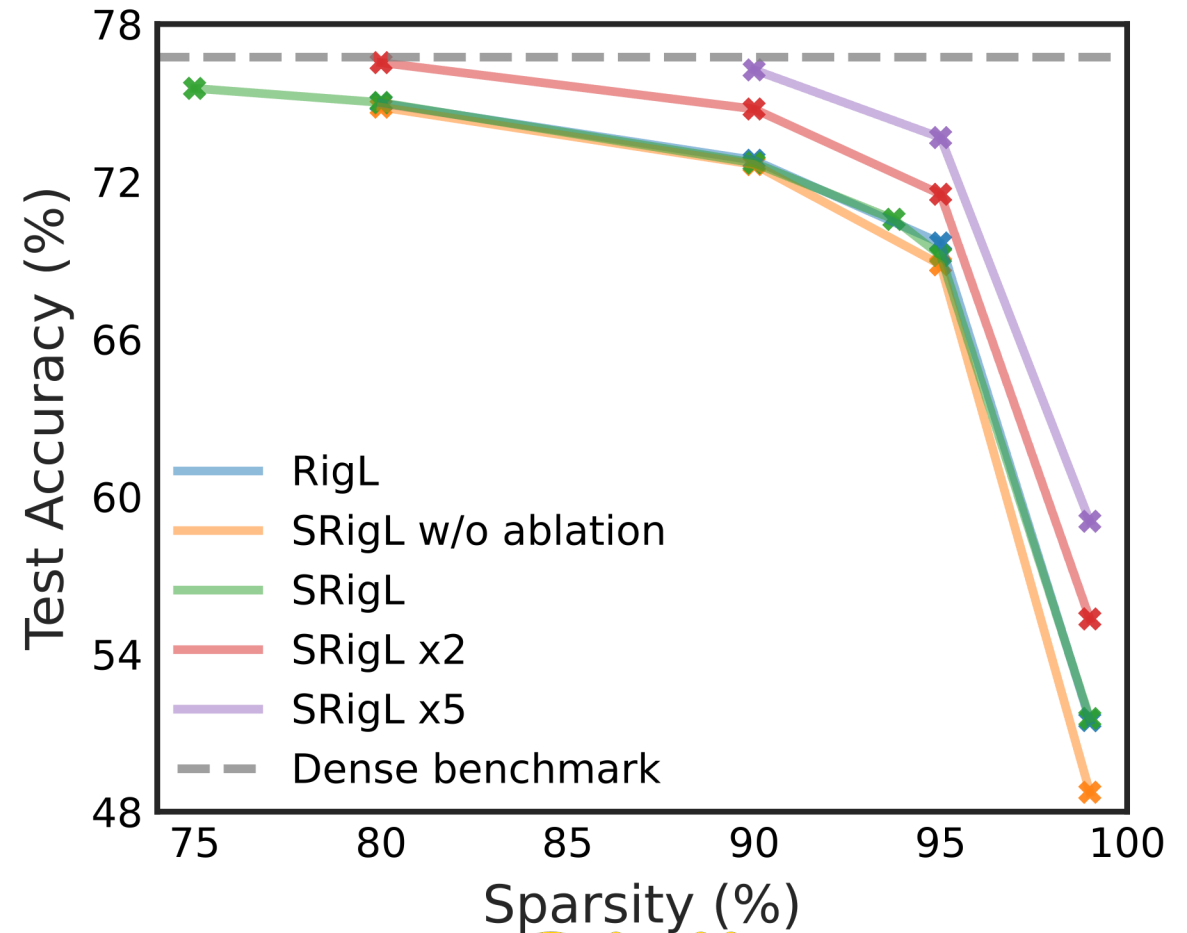


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$W_{0,1}$	$W_{0,2}$	$W_{0,3}$
$W_{1,0}$	$W_{1,1}$	$W_{1,2}$

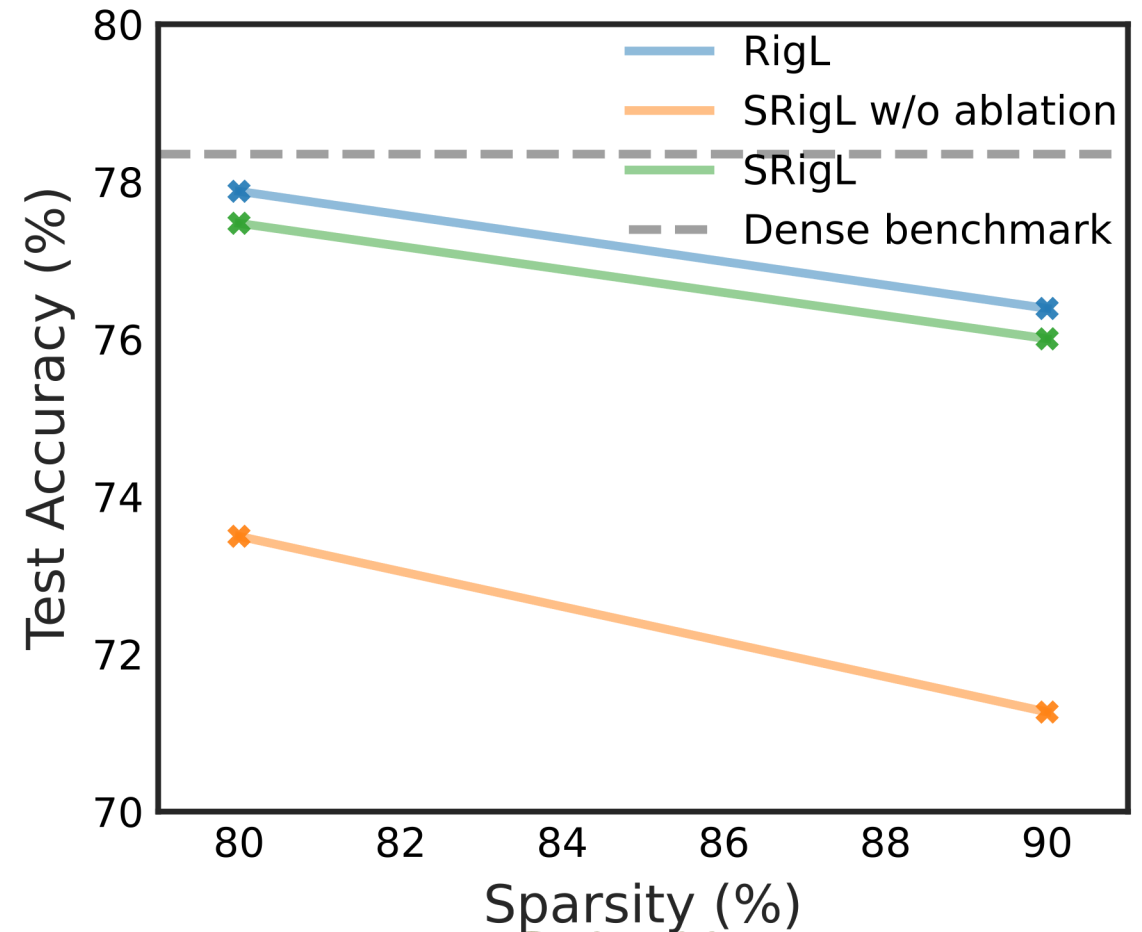
# ImageNet/ResNet-50

- SRigL **matches the performance** of RigL at modest sparsities
- At high sparsities, **ablation is required** to maintain generalization
- Extended training of SRigL w/ablation **matches dense benchmark**, even at 90% sparsity (like RigL)!



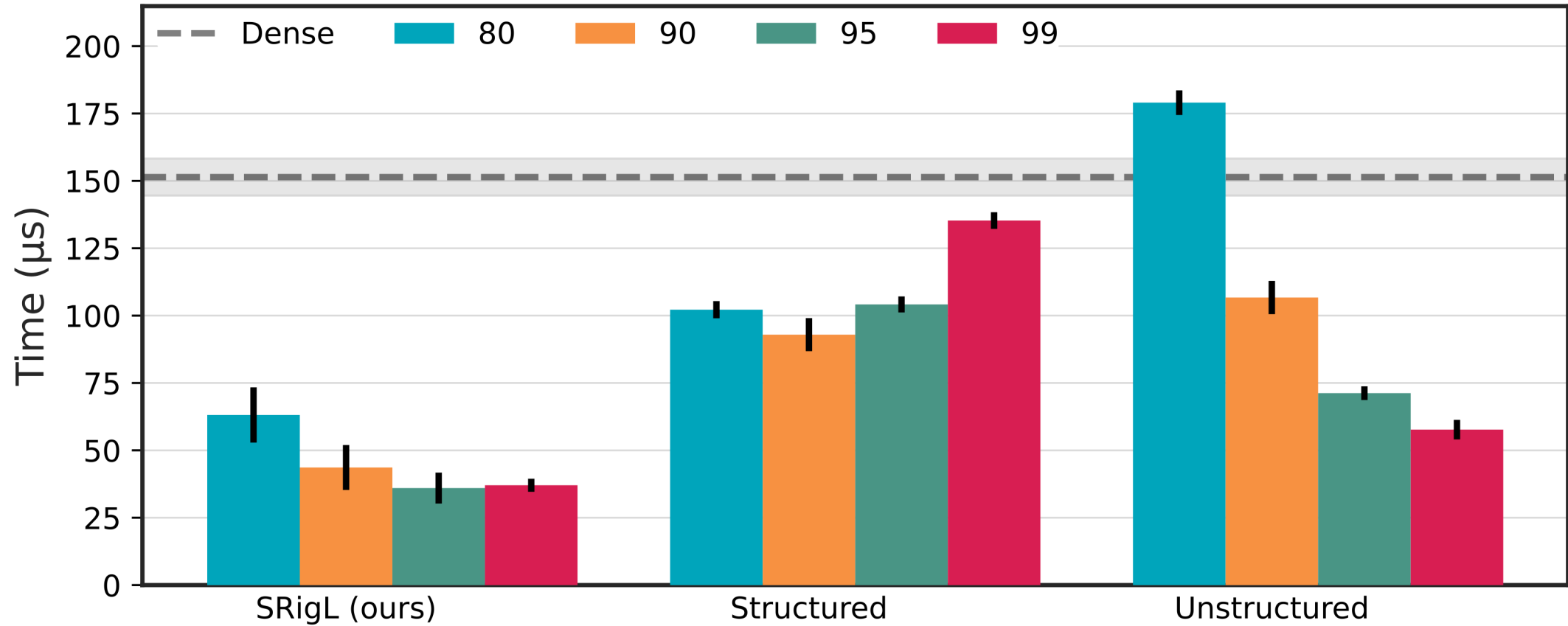
# ImageNet/ViT-B-16

- SRigL also works well with transformer models
- **Neuron ablation is even more effective** with ViT compared to convolutional models

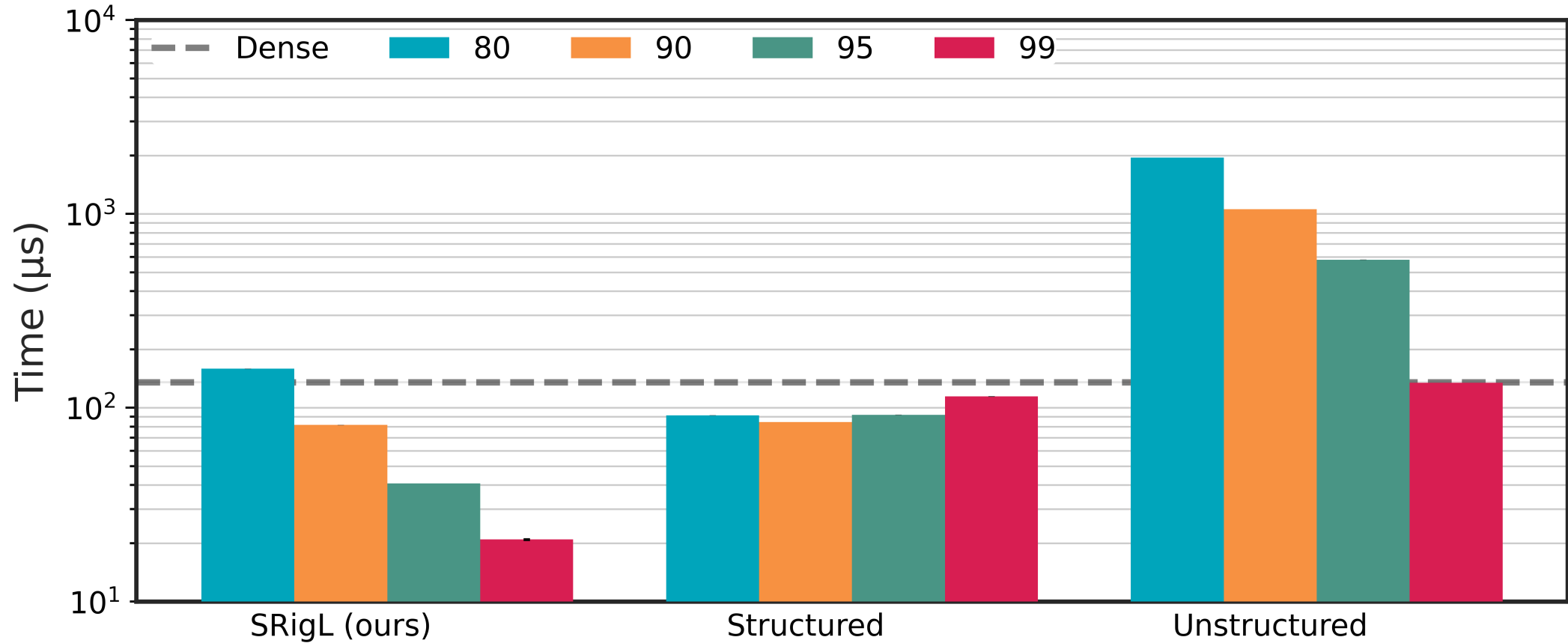




# Acceleration - CPU, batch size = 1



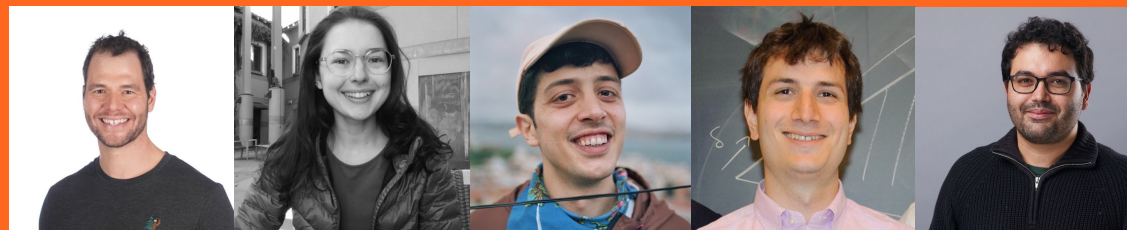
# Acceleration - GPU, batch size = 2048





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Thank you!



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